

Examination Section
II, III/IV B.Tech II Sem Supplementary Lab Examination Time Table JANUARY - 2021 (Y17 Batch)
Computer Science & Engineering

Sub. Code	Regd. No.	Total	Date of Exam	Timings
DESIGN THINKING AND ITS APPLICATIONS 17CS3292	170030534 170031585	2	27/02/2021	09.00 AM to 11.00 AM

Electronics and Computer Science Engineering

FUNDAMENTALS OF COMMUNICATION SYSTEMS 17EM3205	170050036	1	25/02/2021	09.00 AM to 11.00 AM
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Electrical & Electronics Engineering

AC ROTATING MACHINES 17EE3103	170060044 170060079	2	27/02/2021	03.00 PM to 05.00 PM
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Mechanical Engineering

AUTOTRONICS 17ME4082	170070028	1	25/02/2021	03.00 PM to 05.00 PM
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